

Faculty and Research Interests

黃元欣

Yuan-Shin Hwang

副教授

Phone : (02)27376746

Fax : (02)2730-1081

Email : shin@csie.ntust.edu.tw



Education:

**Ph.D. in Computer Science, University of Maryland,
1998**

Experience:

2008-present Associate Professor, Dept. of CSIE,
National Taiwan U. of Science & Technology

2003-2008 Associate Professor, Dept. of CSIE,
National Taiwan Ocean University

2000-2003 Assistant Professor, Dept. of Computer
Science, National Taiwan Ocean University

1999-2000 Assistant Professor, Dept. of CSIE, National
Chi Nan University

Research Areas:

**Compilers, Parallel Processing, Computer
Architecture, Embedded Systems**

Projects:

- **Compiler and Hardware Support for a 256-Register Alpha Processor**
 - develops an instruction encoding format that can significantly increase the numbers of architectural registers visible to programmers and compilers, without adding any extra bits to instructions.
- **System Software Designs for Heterogeneous Embedded Multicore Processors — A1. Compiler Tools for Multicores**
 - Develops a tool chain for multicore systems, including a multicore compiler, a debuggers, and an IDE

