

Faculty and Research Interests

謝仁偉

Jen-Wei Hsieh

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Education :

2006 PhD, Department of Computer Science and Information Engineering, National Taiwan University

Experience :

2012/8~ Associate Professor of Department of Computer Science and Information Engineering, National Taiwan University of Science and Technology

2008/2~2012/7 Assistant Professor of Department of Computer Science and Information Engineering, National Taiwan University of Science and Technology

2007/2~2008/2 Assistant Professor of Department of Computer Science and Information Engineering, National Chiayi University

Research Field :

Real-Time Systems, Non-Volatile Memory Storage Systems

Honor :

- Deputy Team Leaders of 2013 IOI, Taiwan.
- Supervisor of 2012 IICM Best Thesis Award.
- Supervisor of 2012 TISA Master Thesis Award.
- Three-year's project grant from National Science Council for excellent junior research investigators, Taiwan (2012~2015).
- Invited Speaker in the DTF 2012 Taiwan
- Invited paper in IEEE MWSCAS 2011.
- Invited paper in ASP-DAC 2011.
- Invited Speaker in ISOM 2010.
- Honorary Member of the Phi Tau Phi Scholastic Honor Society at National Taiwan University, 2006

Journal Papers : (within 4 years)

- "Block-based Multi-version B⁺-Tree for Flash-based Embedded Database Systems," accept and to appear in IEEE TC.
- "Multi-Channel Architecture-based FTL for Reliable and High-Performance SSD," accept and to appear in IEEE TC.
- "VAST: Virtually Associative Sector Translation for MLC Storage Systems," IEEE TCAD 2013.
- "Implementation Strategy for Downgraded Flash-Memory Storage Devices," ACM TECS 2013.
- "MFTL: A Design and Implementation for MLC Flash Memory Storage Systems," ACM TOS 2012.
- "A Strategy to Emulate NOR Flash with NAND Flash," ACM TOS 2010.
- "Improving Flash Wear-Leveling by Proactively Moving Static Data," IEEE TC 2010.